



PORTAL

USPTO

[Subscribe \(Full Service\)](#)

[Register \(Li Free\)](#)

Search: [The ACM Digital Library](#)

+sigma +delta +subtracts

THE ACM DIGITAL LIBRARY

[Feedback](#) [Report a problem](#)

Terms used **sigma delta subtracts**

Sort results by

[Save results to a Binder](#)

[Try an Ad](#)

Display results

[Search Tips](#)

[Try this se](#)

[Open results in a new window](#)

Results 1 - 20 of 42

Result page: **1** [2](#) [3](#) [next](#)

1 [Wireless telecom silicon integration: analog design for radio, baseband and spectrum](#)

J. Sevenhuijsen, D. Haspeslagh, J. Wenin

January 1998 **Wireless Networks**, Volume 4 Issue 1

Publisher: Kluwer Academic Publishers

Full text available: [pdf\(324.74 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

The application today, pushing analog design for CMOS and RF-bipolar is definitely the mobile radio telephony. New telecom systems like GSM, P the loop...are all developing very rapidly and will enable us very soon to telephone network with full coverage for your car, as well as in your kit desk. In Europe the major telecom companies have worked together to standard for cellular mobi ...

2 [Circuit methods for the integration of low voltage \(1.1-1.8V\) analog](#)

[a-chip IC's in a single-poly CMOS processes](#)

Vladimir Koifman, Yachin Afek, Joseph Shor

August 1999 **Proceedings of the 1999 international symposium on VLSI technology, design and technology ISLPED '99**

Publisher: ACM Press

Full text available: [pdf\(386.49 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

3 The HiBall Tracker: high-performance wide-area tracking for virtual environments

◆ Greg Welch, Gary Bishop, Leandra Vicci, Stephen Brumback, Kurtis Keller, December 1999 **Proceedings of the ACM symposium on Virtual reality technology VRST '99**

Publisher: ACM Press

Full text available: [pdf\(2.01 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index](#)

Our HiBall Tracking System generates over 2000 head-pose estimates per one millisecond of latency, and less than 0.5 millimeters and 0.02 degrees of orientation noise, everywhere in a 4.5 by 8.5 meter room. The system is fast and robust, enabling VR applications and experiments that previously were even impossible. Previously we published descriptions of only the Kalman filter approach that ...

Keywords: Kalman filter, autocalibration, calibration, delay, latency, orientation, tracking, virtual environments

4 A sigma-delta modulation based BIST scheme for mixed-signal circuits

◆ Jiun-Lang Huang, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation ASP-DAC '00**

Publisher: ACM Press

Full text available: [pdf\(117.31 KB\)](#)

Additional Information: [full citation](#)

5 A BIST scheme for on-chip ADC and DAC testing

◆ Jiun-Lang Huang, Chee-Kian Ong, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available: [pdf\(114.67 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [terms](#)

[Publisher](#)

[Site](#)

6 A design strategy for low-voltage low-power continuous-time sigma-

F. Gerfers, Y. Manoli

**March 2001 Proceedings of the conference on Design, automation
DATE '01****Publisher:** IEEE PressFull text available:  [pdf\(619.81 KB\)](#) Additional Information: [full citation, terms](#)**7 Low-voltage low-power switched-current circuits and systems**

Nianxiong Tan, S. Eriksson

March 1995 Proceedings of the 1995 European conference on De**Publisher:** IEEE Computer SocietyFull text available:  [pdf\(642.50 KB\)](#)**Publisher****Site**Additional Information: [full citation, terms](#)

This paper presents low-voltage low-power switched-current circuits and systems. The configuration and common-mode feedforward are the essence. A delay-oversampling A/D converter, and chopper-stabilized oversampling A/D converter are implemented. Measurement results are presented as well.

Keywords: CMOS IC, CMOS analogue integrated circuits, LV switched-current cell, analogue processing circuits, analogue storage, analogue-digital conversion, stabilized oversampling ADC, class AB configuration, common-mode feedforward lines, feedforward, low-power switched-current circuits, oversampling A/D converter, switched current circuits

8 Theory of PLL fractional-N frequency synthesizers

A. Marques, M. Steyaert, W. Sansen

January 1998 Wireless Networks, Volume 4 Issue 1**Publisher:** Kluwer Academic PublishersFull text available:  [pdf\(482.69 KB\)](#) Additional Information: [full citation, index terms](#)

This paper presents an overview of the evolution of frequency synthesizers.

loops (PLLs). The main limitations of the digital PLLs are described, and of using fractional-N techniques is justified. The origin of the typical spurious sidelobes of the synthesized frequency is explained. It is shown how to noise lines by using digital Delta\Sigma modulators to control the frequency. Finally ...

9 Detection of defective sensor elements using $\Sigma\Delta$ -modulation and a

◆ D. Weiler, O. Machul, D. Hammerschmidt, B. J. Hosticka

◆ **January 2000 Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available:  [pdf\(88.57 KB\)](#)

[KB\)](#) 
[Publisher](#)
[Site](#)

Additional Information: [full citation](#),

10 An analysis of selected computer interchange color spaces

◆ James M. Kasson, Wil Plouffe

◆ **October 1992 ACM Transactions on Graphics (TOG), Volume 11 Issue 3**

Publisher: ACM Press

Full text available:  [pdf\(8.77 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index](#)

Important standards for device-independent color allow many different freedom obliges users of these standards to choose the color space in which data. A device-independent interchange color space must exhibit an exact colorimetric color representation, ability to encode all visible colors, con given accuracy, and low computational cost for transforms to and from The performance of CIE 1931 ...

Keywords: CIE 1931 XYZ, CIELAB, CIELUV, SMPTE-C RGB, YCbCr, YES spaces, device-independent color, quantization

11 A low power high performance switched-current multiplier

D. M. W. Leenaerts, G. H. M. Joordens, J. A. Hegelund

◆ **August 1996 Proceedings of the 1996 international symposium on VLSI design ISLPED '96**

Publisher: IEEE Press

Full text available:  [pdf\(77.12 KB\)](#) Additional Information: [full citation](#)

12 Video-based rendering: Video-based rendering

◆ Marcus Magnor, Marc Pollefeys, German Cheung, Wojciech Matusik, Christ
July 2005 **ACM SIGGRAPH 2005 Courses SIGGRAPH '05**

Publisher: ACM Press

Full text available:  [pdf\(5.15 MB\)](#) Additional Information: [full citation](#)

13 Proceedings of the SIGNUM conference on the programming envirc

◆ of numerical software

March 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(5.02 MB\)](#) Additional Information: [full citation](#)

14 Low power signal processing architectures for network microsensor

◆ Michael J. Dong, K. Geoffrey Yung, William J. Kaiser

August 1997 **Proceedings of the 1997 international symposium o
and design ISLPED '97**

Publisher: ACM Press

Full text available:  [pdf\(613.09 KB\)](#) Additional Information: [full citation](#)

15 A two-layer library-based approach to synthesis of analog systems 1

◆ specifications

Alex Doboli, Nagu Dhanwada, Adrian Nunez-Aldana, Ranga Vemuri

April 2004 **ACM Transactions on Design Automation of Electronic
Volume 9 Issue 2**

Publisher: ACM Press

Full text available:  [pdf\(658.00\)](#) Additional Information: [full citation](#)

KB)citings, inde

This paper presents a synthesis methodology for analog systems described in a high-level language. Synthesis produces net-lists of analog components that are sized so that specified objectives (like AC response, signal to noise ratio) are optimized. The gap between abstract specifications and implementation is closed by a layered methodology. The first layer is architecture generation. The second layer is synthesis and constraint ...

Keywords: Analog synthesis, VHDL-AMS, branch-and-bound, genetic algorithm, estimation

16 Experiments with the M & N tree-searching program

◆ James R. Slagle, John K. Dixon

March 1970 **Communications of the ACM**, Volume 13 Issue 3

Publisher: ACM Press

Full text available: [pdf\(896.52 KB\)](#) Additional Information: [full citation](#), [citings](#)

The M & N procedure is an improvement to the mini-max backing-up procedure used in computer programs for game-playing and other purposes. It is based on the idea that it is desirable to have many options when making decisions in the face of uncertainty. The procedure assigns to a MAX (MIN) node the value of the highest (lowest) valued successor node. The M & N procedure assigns to a MAX (MIN) node some function of the highest (lowest) valued successors ...

Keywords: LISP, artificial intelligence, backing-up procedures, decision theory, heuristic program, kalah, min-max backing-up procedure, tree searchin

17 High dynamic range imaging

◆ Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik

August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(20.22 MB\)](#) Additional Information: [full citation](#), [citings](#)

Current display devices can display only a limited range of contrast and color. The main reasons that most image acquisition, processing, and display technologies use only eight bits per color channel. This course outlines recent advances in high dynamic range imaging, from capture to display, that remove this restriction, thereby enabling images with a much wider range of contrast and color.

color gamut and dynamic range of the original scene rather than the current monitor ...

18 Tutorial: abstraction in numerical methods

◆ Gerald Jay Sussman, Matthew Halfant

◆ **January 1988 Proceedings of the 1988 ACM conference on LISP & programming LFP '88**

Publisher: ACM Press

Full text available:  [pdf\(673.99 KB\)](#) Additional Information: [full citation](#), [citations](#), [index terms](#)

We illustrate how the liberal use of high-order procedural abstractions allows us to express some of the vocabulary and methods of numerical analysis. We present a toolbox encapsulating the technique of Richardson extrapolation, and we show how it can be applied to problems of numerical integration and differentiation. By separating the extrapolation from its use in particular circumstances we indicate how numerical methods can be written that exhibit ...

19 BIST and production testing of ADCs using imprecise stimulus

◆ Kumar Parthasarathy, Turker Kuyel, Dana Price, Le Jin, Degang Chen, Rar

◆ **October 2003 ACM Transactions on Design Automation of Electronic Circuits and Systems** Volume 8 Issue 4

Publisher: ACM Press

Full text available:  [pdf\(500.33 KB\)](#) Additional Information: [full citation](#), [citations](#), [index terms](#)

A new approach for testing mixed-signal circuits based upon using imprecise stimuli. Unlike most existing Built-In Self-Test (BIST) and production test approaches, which require excitation signals that are at least 3 bits or more linear than the Device under test, the proposed approach can work with stimuli that are several bits less linear. This dramatically reduces the requirements on stimulus generation for BIST. The potential for using inexpensive ...

Keywords: ADC linearity, Analog and mixed-signal testing, built-in self-test, imprecision stimulus, production test

20 Systems & applications II: Real-time eye detection and tracking under difficult conditions

◆ Zhiwei Zhu, Kikuo Fujimura, Qiang Ji

◆ **March 2002 Proceedings of the 2002 symposium on Eye tracking and related applications ETRA '02**

Publisher: ACM Press

Full text available: [pdf\(602.92 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

Non-intrusive methods based on active remote IR illumination for eye tracking have many applications of vision-based man-machine interaction. One problem with these methods is their sensitivity to lighting condition change. This tends to significantly affect the performance of application. In this paper, we present a new real-time eye detection and tracking algorithm that works under variable and realistic lighting conditions. Based on color information, the proposed method can effectively detect and track eyes even in complex environments. Experimental results show that the proposed method is robust and effective.

Keywords: Eye Tracking, Kalman Filter, Mean Shift, Support Vector Machine

Results 1 - 20 of 42

Result page: [1](#) [2](#) [3](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2003 ACM, Inc. All rights reserved.
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads: [Adobe Acrobat](#) [QuickTime](#) [Windows Media Player](#)



USPTO

[Subscribe \(Full Service\)](#)[Register \(Li Free\)](#)

Search: [+sigma +delta +subtracting](#)

THE ACM DIGITAL LIBRARY

 [Feedback](#) [Report a problem](#)

Terms used sigma delta subtracting

Sort results by [relevance](#) [Save results to a Binder](#)[Try an Ad](#)Display results [expanded form](#) [Search Tips](#)[Try this se](#) [Open results in a new window](#)

Results 1 - 20 of 42

Result page: [1](#) [2](#) [3](#) [next](#)

1 Wireless telecom silicon integration: analog design for radio, baseband and spectrum

J. Sevenhans, D. Haspeslagh, J. Wenin

January 1998 **Wireless Networks**, Volume 4 Issue 1**Publisher:** Kluwer Academic Publishers

Full text available: [pdf\(324.74 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

The application today, pushing analog design for CMOS and RF-bipolar is definitely the mobile radio telephony. New telecom systems like GSM, PDC, TDMA...are all developing very rapidly and will enable us very soon to have a telephone network with full coverage for your car, as well as in your kitchen, at your desk. In Europe the major telecom companies have worked together to define a standard for cellular mobile ...

2 Circuit methods for the integration of low voltage (1.1-1.8V) analog I/O

[a-chip IC's in a single-poly CMOS processes](#)

Vladimir Koifman, Yachin Afek, Joseph Shor

August 1999 **Proceedings of the 1999 international symposium on low power design ISLPED '99****Publisher:** ACM Press

Full text available: [pdf\(386.49 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

3 The HiBall Tracker: high-performance wide-area tracking for virtual environments

◆ Greg Welch, Gary Bishop, Leandra Vicci, Stephen Brumback, Kurtis Keller, December 1999 **Proceedings of the ACM symposium on Virtual reality technology VRST '99**

Publisher: ACM Press

Full text available: [pdf\(2.01 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index](#)

Our HiBall Tracking System generates over 2000 head-pose estimates per one millisecond of latency, and less than 0.5 millimeters and 0.02 degrees orientation noise, everywhere in a 4.5 by 8.5 meter room. The system is fast and robust, enabling VR applications and experiments that previously were even impossible. Previously we published descriptions of only the Kalman filter approach that ...

Keywords: Kalman filter, autocalibration, calibration, delay, latency, orientation, tracking, virtual environments

4 A sigma-delta modulation based BIST scheme for mixed-signal circuits

◆ Jiun-Lang Huang, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation ASP-DAC '00**

Publisher: ACM Press

Full text available: [pdf\(117.31 KB\)](#)

Additional Information: [full citation](#)

5 A BIST scheme for on-chip ADC and DAC testing

◆ Jiun-Lang Huang, Chee-Kian Ong, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available: [pdf\(114.67 KB\)](#)



[Publisher](#)

[Site](#)

Additional Information: [full citation](#), [citations](#), [index](#)

[terms](#)

6 A design strategy for low-voltage low-power continuous-time sigma-

F. Gerfers, Y. Manoli

**March 2001 Proceedings of the conference on Design, automation
DATE '01****Publisher:** IEEE PressFull text available:  [pdf\(619.81 KB\)](#) Additional Information: [full citation, index terms](#)**7 Low-voltage low-power switched-current circuits and systems**

Nianxiong Tan, S. Eriksson

March 1995 Proceedings of the 1995 European conference on De**Publisher:** IEEE Computer SocietyFull text available:  [pdf\(642.50 KB\)](#)[Publisher](#)
[Site](#)Additional Information: [full citation, index terms](#)

This paper presents low-voltage low-power switched-current circuits and systems. The configuration and common-mode feedforward are the essence. A delay-oversampling A/D converter, and chopper-stabilized oversampling A/D converter are implemented. Measurement results are presented as well.

Keywords: CMOS IC, CMOS analogue integrated circuits, LV switched-current cell, analogue processing circuits, analogue storage, analogue-digital conversion, stabilized oversampling ADC, class AB configuration, common-mode feedforward lines, feedforward, low-power switched-current circuits, oversampling A/D converter, switched current circuits

8 Theory of PLL fractional-N frequency synthesizers

A. Marques, M. Steyaert, W. Sansen

January 1998 Wireless Networks, Volume 4 Issue 1**Publisher:** Kluwer Academic PublishersFull text available:  [pdf\(482.69 KB\)](#) Additional Information: [full citation, index terms](#)

This paper presents an overview of the evolution of frequency synthesizers.

loops (PLLs). The main limitations of the digital PLLs are described, and of using fractional-N techniques is justified. The origin of the typical spurious sidelobes of the synthesized frequency is explained. It is shown how to noise lines by using digital Delta\|Sigma modulators to control the frequency. Finally ...

9 Detection of defective sensor elements using $\Sigma\Delta$ -modulation and a

◆ D. Weiler, O. Machul, D. Hammerschmidt, B. J. Hosticka

◆ **January 2000 Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available: [pdf\(88.57](#)

KB)

[Publisher](#)

[Site](#)

Additional Information: [full citation](#),

10 An analysis of selected computer interchange color spaces

◆ James M. Kasson, Wil Plouffe

◆ **October 1992 ACM Transactions on Graphics (TOG), Volume 11 Issue 3**

Publisher: ACM Press

Full text available: [pdf\(8.77](#)

MB)

Additional Information: [full citation](#),

[citations](#), [index](#)

Important standards for device-independent color allow many different freedom obliges users of these standards to choose the color space in which they work. A device-independent interchange color space must exhibit an exact colorimetric color representation, ability to encode all visible colors, conve given accuracy, and low computational cost for transforms to and from The performance of CIE 1931 ...

Keywords: CIE 1931 XYZ, CIELAB, CIELUV, SMPTE-C RGB, YCbCr, YES spaces, device-independent color, quantization

11 A low power high performance switched-current multiplier

D. M. W. Leenaerts, G. H. M. Joordens, J. A. Hegelund

◆ **August 1996 Proceedings of the 1996 international symposium on VLSI design and design ISLPED '96**

Publisher: IEEE Press

Full text available: [pdf\(77.12 KB\)](#) Additional Information: [full citation](#)

12 Video-based rendering: Video-based rendering

◆ Marcus Magnor, Marc Pollefeys, German Cheung, Wojciech Matusik, Christ
July 2005 **ACM SIGGRAPH 2005 Courses SIGGRAPH '05**

Publisher: ACM Press

Full text available: [pdf\(5.15 MB\)](#) Additional Information: [full citation](#)

13 Proceedings of the SIGNUM conference on the programming envirc

◆ of numerical software

March 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1

Publisher: ACM Press

Full text available: [pdf\(5.02 MB\)](#) Additional Information: [full citation](#)

14 Low power signal processing architectures for network microsensor

◆ Michael J. Dong, K. Geoffrey Yung, William J. Kaiser

August 1997 **Proceedings of the 1997 international symposium o
and design ISLPED '97**

Publisher: ACM Press

Full text available: [pdf\(613.09 KB\)](#) Additional Information: [full citation](#)

15 A two-layer library-based approach to synthesis of analog systems i

◆ specifications

Alex Doboli, Nagu Dhanwada, Adrian Nunez-Aldana, Ranga Vemuri

April 2004 **ACM Transactions on Design Automation of Electronic
Volume 9 Issue 2**

Publisher: ACM Press

Full text available: [pdf\(658.00\)](#) Additional Information: [full citation](#)

KB)citings, inde

This paper presents a synthesis methodology for analog systems described in a high-level language. Synthesis produces net-lists of analog components that are sized so that specified objectives (like AC response, signal to noise ratio) are optimized. The gap between abstract specifications and implementation is closed by a layered methodology. The first layer is architecture generation. The second layer is synthesis and constraint ...

Keywords: Analog synthesis, VHDL-AMS, branch-and-bound, genetic algorithm, estimation

16 Experiments with the M & N tree-searching program

◆ James R. Slagle, John K. Dixon

March 1970 **Communications of the ACM**, Volume 13 Issue 3

Publisher: ACM Press

Full text available: [pdf\(896.52 KB\)](#) Additional Information: [full citation](#), [citings](#)

The M & N procedure is an improvement to the mini-max backing-up procedure used in computer programs for game-playing and other purposes. It is based on the idea that it is desirable to have many options when making decisions in the face of uncertainty. The mini-max procedure assigns to a MAX (MIN) node the value of the highest (lowest) valued successor node. The M & N procedure assigns to a MAX (MIN) node some function of the highest (lowest) valued successors ...

Keywords: LISP, artificial intelligence, backing-up procedures, decision theory, heuristic program, kalah, min-max backing-up procedure, tree searchin

17 High dynamic range imaging

◆ Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik

August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(20.22 MB\)](#) Additional Information: [full citation](#), [citings](#)

Current display devices can display only a limited range of contrast and color. The main reasons that most image acquisition, processing, and display technologies use only eight bits per color channel. This course outlines recent advances in high dynamic range imaging, from capture to display, that remove this restriction, thereby enabling images with a much wider range of contrast and color.

color gamut and dynamic range of the original scene rather than the limit of current monitor ...

18 Tutorial: abstraction in numerical methods

◆ Gerald Jay Sussman, Matthew Halfant

◆ **January 1988 Proceedings of the 1988 ACM conference on LISP and programming LFP '88**

Publisher: ACM Press

Full text available: [pdf\(673.99 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

We illustrate how the liberal use of high-order procedural abstractions allows us to express some of the vocabulary and methods of numerical analysis. We present a toolbox encapsulating the technique of Richardson extrapolation, and we show how it can be used to solve problems of numerical integration and differentiation. By separating the extrapolation from its use in particular circumstances we indicate how numerical methods can be written that exhibit ...

19 BIST and production testing of ADCs using imprecise stimulus

◆ Kumar Parthasarathy, Turker Kuyel, Dana Price, Le Jin, Degang Chen, Rar

◆ **October 2003 ACM Transactions on Design Automation of Electronic Circuits and Systems**
Volume 8 Issue 4

Publisher: ACM Press

Full text available: [pdf\(500.33 KB\)](#) Additional Information: [full citation](#), [citations](#), [index terms](#)

A new approach for testing mixed-signal circuits based upon using imprecise stimuli is presented. Unlike most existing Built-In Self-Test (BIST) and production test approaches, this approach uses excitation signals that are at least 3 bits or more linear than the Device Under Test (DUT). The proposed approach can work with stimuli that are several bits less linear than the DUT. This dramatically reduces the requirements on stimulus generation for BIST. The potential for using inexpensive ...

Keywords: ADC linearity, Analog and mixed-signal testing, built-in self-test, calibration, measurement, imprecision stimulus, production test

20 Systems & applications II: Real-time eye detection and tracking under difficult conditions

◆ Zhiwei Zhu, Kikuo Fujimura, Qiang Ji

◆ **March 2002 Proceedings of the 2002 symposium on Eye tracking and related applications ETRA '02**

Publisher: ACM Press

Full text available:  [pdf\(602.92 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

Non-intrusive methods based on active remote IR illumination for eye tracking have many applications of vision-based man-machine interaction. One problem with these methods is their sensitivity to lighting condition change. This tends to significantly affect the performance of application. In this paper, we present a new real-time eye detection algorithm that works under variable and realistic lighting conditions. Based on color information, the proposed method can effectively detect eyes even in complex environments. Experimental results show that the proposed method is robust to various lighting conditions.

Keywords: Eye Tracking, Kalman Filter, Mean Shift, Support Vector Machine

Results 1 - 20 of 42

Result page: [1](#) [2](#) [3](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2007 ACM, Inc. All rights reserved.
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)



USPTO

[Subscribe \(Full Service\)](#)
[Register \(Li Free\)](#)

Search: [The ACM Digital Library](#)
[+sigma +delta +subtracted](#)

THE ACM DIGITAL LIBRARY

[Feedback](#) [Report a problem](#)

Terms used sigma delta subtracted

Sort results by
 [Save results to a Binder](#)
[Try an Ad](#)
Display results
 [Search Tips](#)
[Try this search](#)
 [Open results in a new window](#)

Results 1 - 20 of 42

Result page: [1](#) [2](#) [3](#) [next](#)

1 [Wireless telecom silicon integration: analog design for radio, baseband and spectrum](#)

J. Sevenhans, D. Haspeslagh, J. Wenin

January 1998 **Wireless Networks**, Volume 4 Issue 1**Publisher:** Kluwer Academic Publishers

Full text available: [pdf\(324.74 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

The application today, pushing analog design for CMOS and RF-bipolar integrated circuits definitely the mobile radio telephony. New telecom systems like GSM, PDC, TDMA, the loop...are all developing very rapidly and will enable us very soon to have a telephone network with full coverage for your car, as well as in your kitbag or on your desk. In Europe the major telecom companies have worked together to define a standard for cellular mobile communications ...

2 [Circuit methods for the integration of low voltage \(1.1-1.8V\) analog functions in a-chip IC's in a single-poly CMOS processes](#)

Vladimir Koifman, Yachin Afek, Joseph Shor

August 1999 **Proceedings of the 1999 international symposium on low power design ISLPED '99****Publisher:** ACM Press

Full text available: [pdf\(386.49 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

3 The HiBall Tracker: high-performance wide-area tracking for virtual environments

◆ Greg Welch, Gary Bishop, Leandra Vicci, Stephen Brumback, Kurtis Keller, December 1999 **Proceedings of the ACM symposium on Virtual reality technology VRST '99**

Publisher: ACM Press

Full text available: [pdf\(2.01 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index](#)

Our HiBall Tracking System generates over 2000 head-pose estimates per one millisecond of latency, and less than 0.5 millimeters and 0.02 degrees orientation noise, everywhere in a 4.5 by 8.5 meter room. The system is fast and robust, enabling VR applications and experiments that previously were even impossible. Previously we published descriptions of only the Kalman filter approach that ...

Keywords: Kalman filter, autocalibration, calibration, delay, latency, orientation, tracking, virtual environments

4 A sigma-delta modulation based BIST scheme for mixed-signal circuits

◆ Jiun-Lang Huang, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation ASP-DAC '00**

Publisher: ACM Press

Full text available: [pdf\(117.31 KB\)](#)

Additional Information: [full citation](#)

5 A BIST scheme for on-chip ADC and DAC testing

◆ Jiun-Lang Huang, Chee-Kian Ong, Kwang-Ting Cheng

◆ January 2000 **Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available: [pdf\(114.67 KB\)](#)

[Publisher](#)

[Site](#)

Additional Information: [full citation](#), [citations](#), [index](#)

[terms](#)

6 A design strategy for low-voltage low-power continuous-time sigma-

F. Gerfers, Y. Manoli

**March 2001 Proceedings of the conference on Design, automation
DATE '01****Publisher:** IEEE PressFull text available:  [pdf\(619.81 KB\)](#) Additional Information: [full citation, terms](#)**7 Low-voltage low-power switched-current circuits and systems**

Nianxiong Tan, S. Eriksson

March 1995 Proceedings of the 1995 European conference on De**Publisher:** IEEE Computer SocietyFull text available:  [pdf\(642.50 KB\)](#)[Publisher](#)
[Site](#)Additional Information: [full citation,](#)

This paper presents low-voltage low-power switched-current circuits and systems. The configuration and common-mode feedforward are the essence. A delay-oversampling A/D converter, and chopper-stabilized oversampling A/D converter are implemented. Measurement results are presented as well.

Keywords: CMOS IC, CMOS analogue integrated circuits, LV switched-current cell, analogue processing circuits, analogue storage, analogue-digital conversion, stabilized oversampling ADC, class AB configuration, common-mode feedforward lines, feedforward, low-power switched-current circuits, oversampling A/D converter, switched current circuits

8 Theory of PLL fractional-N frequency synthesizers

A. Marques, M. Steyaert, W. Sansen

January 1998 Wireless Networks, Volume 4 Issue 1**Publisher:** Kluwer Academic PublishersFull text available:  [pdf\(482.69 KB\)](#) Additional Information: [full citation, index terms](#)

This paper presents an overview of the evolution of frequency synthesizers.

loops (PLLs). The main limitations of the digital PLLs are described, and of using fractional-N techniques is justified. The origin of the typical spurious sidelobes of the synthesized frequency is explained. It is shown how to noise lines by using digital \Delta\Sigma modulators to control the frequency. Finally ...

9 Detection of defective sensor elements using $\Sigma\Delta$ -modulation and a

◆ D. Weiler, O. Machul, D. Hammerschmidt, B. J. Hosticka

◆ **January 2000 Proceedings of the conference on Design, automation and test in Europe DATE '00**

Publisher: ACM Press

Full text available: [pdf\(88.57 KB\)](#)

[Publisher Site](#)

Additional Information: [full citation](#),

10 An analysis of selected computer interchange color spaces

◆ James M. Kasson, Wil Plouffe

◆ **October 1992 ACM Transactions on Graphics (TOG), Volume 11 Issue 3**

Publisher: ACM Press

Full text available: [pdf\(8.77 MB\)](#)

Additional Information: [full citation](#), [citations](#), [index](#)

Important standards for device-independent color allow many different freedom obliges users of these standards to choose the color space in which data. A device-independent interchange color space must exhibit an exact colorimetric color representation, ability to encode all visible colors, color given accuracy, and low computational cost for transforms to and from The performance of CIE 1931 ...

Keywords: CIE 1931 XYZ, CIELAB, CIELUV, SMPTE-C RGB, YCbCr, YES spaces, device-independent color, quantization

11 A low power high performance switched-current multiplier

D. M. W. Leenaerts, G. H. M. Joordens, J. A. Hegelund

◆ **August 1996 Proceedings of the 1996 international symposium on VLSI design and design ISLPED '96**

Publisher: IEEE Press

Full text available:  [pdf\(77.12 KB\)](#) Additional Information: [full citation](#)

12 Video-based rendering: Video-based rendering

◆ Marcus Magnor, Marc Pollefeys, German Cheung, Wojciech Matusik, Christ
July 2005 **ACM SIGGRAPH 2005 Courses SIGGRAPH '05**

Publisher: ACM Press

Full text available:  [pdf\(5.15 MB\)](#) Additional Information: [full citation](#)

13 Proceedings of the SIGNUM conference on the programming envirc

◆ of numerical software

March 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(5.02 MB\)](#) Additional Information: [full citation](#)

14 Low power signal processing architectures for network microsensor

◆ Michael J. Dong, K. Geoffrey Yung, William J. Kaiser

August 1997 **Proceedings of the 1997 international symposium o
and design ISLPED '97**

Publisher: ACM Press

Full text available:  [pdf\(613.09 KB\)](#) Additional Information: [full citation](#)

15 A two-layer library-based approach to synthesis of analog systems 1

◆ specifications

Alex Doboli, Nagu Dhanwada, Adrian Nunez-Aldana, Ranga Vemuri

April 2004 **ACM Transactions on Design Automation of Electronic
Volume 9 Issue 2**

Publisher: ACM Press

Full text available:  [pdf\(658.00\)](#) Additional Information: [full citation](#)

KB)citings, inde

This paper presents a synthesis methodology for analog systems described in VHDL-AMS. Synthesis produces net-lists of analog components that are sized so that specified objectives (like AC response, signal to noise ratio) are optimized. The gap between abstract specifications and implementation is closed by a layered methodology. The first layer is architecture generation. The second layer is synthesis and constraint ...

Keywords: Analog synthesis, VHDL-AMS, branch-and-bound, genetic algorithm, estimation

16 Experiments with the M & N tree-searching program

◆ James R. Slagle, John K. Dixon

March 1970 **Communications of the ACM**, Volume 13 Issue 3

Publisher: ACM Press

Full text available: [pdf\(896.52 KB\)](#) Additional Information: [full citation](#), [citings](#)

The M & N procedure is an improvement to the mini-max backing-up procedure used in computer programs for game-playing and other purposes. It is based on the idea that it is desirable to have many options when making decisions in the face of uncertainty. The procedure assigns to a MAX (MIN) node the value of the highest (lowest) valued successor node. The M & N procedure assigns to a MAX (MIN) node some function of the highest (lowest) valued successors ...

Keywords: LISP, artificial intelligence, backing-up procedures, decision theory, heuristic program, kalah, min-max backing-up procedure, tree searchin

17 High dynamic range imaging

◆ Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik

August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(20.22 MB\)](#) Additional Information: [full citation](#), [citings](#)

Current display devices can display only a limited range of contrast and color. The main reasons that most image acquisition, processing, and display technologies use only eight bits per color channel. This course outlines recent advances in high dynamic range imaging, from capture to display, that remove this restriction, thereby enabling images with a much wider range of contrast and color.

color gamut and dynamic range of the original scene rather than the limited current monitor ...

18 Tutorial: abstraction in numerical methods

◆ Gerald Jay Sussman, Matthew Halfant

◆ **January 1988 Proceedings of the 1988 ACM conference on LISP & programming LFP '88**

Publisher: ACM Press

Full text available:  [pdf\(673.99 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

We illustrate how the liberal use of high-order procedural abstractions allows us to express some of the vocabulary and methods of numerical analysis. A toolbox encapsulating the technique of Richardson extrapolation, and we problems of numerical integration and differentiation. By separating the extrapolation from its use in particular circumstances we indicate how n written that exha ...

19 BIST and production testing of ADCs using imprecise stimulus

◆ Kumar Parthasarathy, Turker Kuyel, Dana Price, Le Jin, Degang Chen, Rar

◆ **October 2003 ACM Transactions on Design Automation of Electronic Circuits and Systems** Volume 8 Issue 4

Publisher: ACM Press

Full text available:  [pdf\(500.33 KB\)](#) Additional Information: [full citation](#), [index terms](#)

A new approach for testing mixed-signal circuits based upon using imprecise stimuli. Unlike most existing Built-In Self-Test (BIST) and production test approaches, which require excitation signals that are at least 3 bits or more linear than the Device under test, the proposed approach can work with stimuli that are several bits less linear. This dramatically reduces the requirements on stimulus generation for BIST and makes it potential for using inexpensive ...

Keywords: ADC linearity, Analog and mixed-signal testing, built-in self-test, calibration, measurement, imprecision stimulus, production test

20 Systems & applications II: Real-time eye detection and tracking under difficult conditions

◆ Zhiwei Zhu, Kikuo Fujimura, Qiang Ji

◆ **March 2002 Proceedings of the 2002 symposium on Eye tracking and related applications ETRA '02**

Publisher: ACM Press

Full text available: [pdf\(602.92 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

Non-intrusive methods based on active remote IR illumination for eye tracking have many applications of vision-based man-machine interaction. One problem with these methods is their sensitivity to lighting condition change. This tends to significantly affect the performance of application. In this paper, we present a new real-time eye detection algorithm that works under variable and realistic lighting conditions. Based on color information, the proposed method can effectively detect eyes even in complex environments. Experimental results show that the proposed method is robust to lighting changes.

Keywords: Eye Tracking, Kalman Filter, Mean Shift, Support Vector Machine

Results 1 - 20 of 42

Result page: [1](#) [2](#) [3](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2003 ACM, Inc. All rights reserved.
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads: [Adobe Acrobat](#) [QuickTime](#) [Windows Media Player](#)



USPTO

[Subscribe \(Full Service\)](#)[Register \(Li Free\)](#)[Search: The ACM Digital Library](#)

+sigma +delta +subtracted +error

THE ACM DIGITAL LIBRARY

[Feedback](#)[Report a problem](#)

Terms used **sigma delta subtracted error**

Sort results by [Save results to a Binder](#)

Try an Ad

Display results [Search Tips](#)

Try this se

 Open results in a new window

Results 1 - 20 of 35

Result page: **1** [2](#) [next](#)

1 [The HiBall Tracker: high-performance wide-area tracking for virtual environments](#)

Greg Welch, Gary Bishop, Leandra Vicci, Stephen Brumback, Kurtis Keller, December 1999 **Proceedings of the ACM symposium on Virtual reality technology VRST '99**

Publisher: ACM PressFull text available: [pdf\(2.01 MB\)](#)Additional Information: [full citation, citations, index](#)

Our HiBall Tracking System generates over 2000 head-pose estimates per one millisecond of latency, and less than 0.5 millimeters and 0.02 degrees of orientation noise, everywhere in a 4.5 by 8.5 meter room. The system is fast and robust, enabling VR applications and experiments that previously were even impossible. Previously we published descriptions of only the Kalman filter approach that ...

Keywords: Kalman filter, autocalibration, calibration, delay, latency, orientation, tracking, virtual environments

2 [A sigma-delta modulation based BIST scheme for mixed-signal circuits](#)

Jiun-Lang Huang, Kwang-Ting Cheng

January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation ASP-DAC '00**

Publisher: ACM Press

Full text available:  [pdf\(117.31 KB\)](#) Additional Information: [full citation](#), [KB](#)

3 A BIST scheme for on-chip ADC and DAC testing

 Jiun-Lang Huang, Chee-Kian Ong, Kwang-Ting Cheng
January 2000 **Proceedings of the conference on Design, automation and test in Europe (DATE '00)**

Publisher: ACM Press

Full text available:  [pdf\(114.67 KB\)](#)

 [Publisher](#)

[Site](#)

Additional Information: [full citation](#), [terms](#)

4 Wireless telecom silicon integration: analog design for radio, baseband and spectrum

J. Sevenhuijsen, D. Haspeslagh, J. Wenzel
January 1998 **Wireless Networks**, Volume 4 Issue 1

Publisher: Kluwer Academic Publishers

Full text available:  [pdf\(324.74 KB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

The application today, pushing analog design for CMOS and RF-bipolar integrated circuits to definitely the mobile radio telephony. New telecom systems like GSM, PDC, TDMA, ISDN, the loop...are all developing very rapidly and will enable us very soon to have a telephone network with full coverage for your car, as well as in your kitchen, at your desk. In Europe the major telecom companies have worked together to define a standard for cellular mobile ...

5 Detection of defective sensor elements using $\Sigma\Delta$ -modulation and a self-test scheme

 D. Weiler, O. Machul, D. Hammerschmidt, B. J. Hosticka
January 2000 **Proceedings of the conference on Design, automation and test in Europe (DATE '00)**

Publisher: ACM Press

Full text available:  [pdf\(88.57 KB\)](#)

 [Publisher](#)

Additional Information: [full citation](#), [terms](#)

Site**6 Theory of PLL fractional-N frequency synthesizers**

A. Marques, M. Steyaert, W. Sansen

January 1998 **Wireless Networks**, Volume 4 Issue 1

Publisher: Kluwer Academic Publishers

Full text available: [pdf\(482.69 KB\)](#) Additional Information: [full citation, index terms](#)

This paper presents an overview of the evolution of frequency synthesizer loops (PLLs). The main limitations of the digital PLLs are described, and of using fractional-N techniques is justified. The origin of the typical spurious sidelobes of the synthesized frequency is explained. It is shown how to noise lines by using digital Delta\Sigma modulators to control the frequency. Finally ...

7 An analysis of selected computer interchange color spaces

 James M. Kasson, Wil Plouffe

October 1992 **ACM Transactions on Graphics (TOG)**, Volume 11 Issue 1

Publisher: ACM Press

Full text available: [pdf\(8.77 MB\)](#) Additional Information: [full citation, citations, index terms](#)

Important standards for device-independent color allow many different freedom obliges users of these standards to choose the color space in which data. A device-independent interchange color space must exhibit an exact colorimetric color representation, ability to encode all visible colors, color given accuracy, and low computational cost for transforms to and from The performance of CIE 1931 ...

Keywords: CIE 1931 XYZ, CIELAB, CIELUV, SMPTE-C RGB, YCbCr, YES spaces, device-independent color, quantization

8 Video-based rendering: Video-based rendering

 Marcus Magnor, Marc Pollefeys, German Cheung, Wojciech Matusik, Christ

July 2005 **ACM SIGGRAPH 2005 Courses SIGGRAPH '05**

Publisher: ACM Press

Full text available: [pdf\(5.15 MB\)](#)

MB)Additional Information: [full citation](#)**9** Proceedings of the SIGNUM conference on the programming envirc
of numerical softwareMarch 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1**Publisher:** ACM PressFull text available:  [pdf\(5.02
MB\)](#)Additional Information: [full citation](#)**10** A design strategy for low-voltage low-power continuous-time sigma-

F. Gerfers, Y. Manoli

March 2001 **Proceedings of the conference on Design, automation
DATE '01****Publisher:** IEEE PressFull text available:  [pdf\(619.81
KB\)](#) Additional Information: [full citation,
terms](#)**11** Pac-bayesian generalisation error bounds for gaussian process clas

Matthias Seeger

March 2003 **The Journal of Machine Learning Research**, Volume 3**Publisher:** MIT PressFull text available:  [pdf\(487.11
KB\)](#) Additional Information: [full citation,
citations, inde](#)

Approximate Bayesian Gaussian process (GP) classification techniques are learning methods, similar in appearance and performance to support vector machines. While simple probabilistic models, they render interpretable results and can be used in various frameworks for model selection, feature selection, etc. In this paper, by extending the theorem of McAllester (1999a), we prove distribution-free generalisation error bounds for a wide range of approxima ...

Keywords: Bayesian learning, Gaussian processes, Gibbs classifier, Kernel, Bayesian framework, convex duality, generalisation error bounds, spars

12 High dynamic range imaging

◆ Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik

◆ August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(20.22 MB\)](#)

Additional Information: [full citation](#),

Current display devices can display only a limited range of contrast and main reasons that most image acquisition, processing, and display tech eight bits per color channel. This course outlines recent advances in hig from capture to display, that remove this restriction, thereby enabling i color gamut and dynamic range of the original scene rather than the lim current monitor ...

13 Low-voltage low-power switched-current circuits and systems

Nianxiong Tan, S. Eriksson

March 1995 **Proceedings of the 1995 European conference on Design of Circuits and Systems**

Publisher: IEEE Computer Society

Full text available: [pdf\(642.50 KB\)](#)

[pdf\(642.50 KB\)](#)

[Publisher](#)

[Site](#)

Additional Information: [full citation](#),

This paper presents low-voltage low-power switched-current circuits and their applications. The key concepts of the switched-current approach, such as configuration and common-mode feedforward are the essence. A delay-locked loop, a switched-current oversampling A/D converter, and chopper-stabilized oversampling A/D converter are designed and implemented. Measurement results are presented as well.

Keywords: CMOS IC, CMOS analogue integrated circuits, LV switched-current cell, analogue processing circuits, analogue storage, analogue-digital conversion, chopper-stabilized oversampling ADC, class AB configuration, common-mode feedforward, low-power switched-current circuits, oversampling A/D converter, switched current circuits

14 Tutorial: abstraction in numerical methods

◆ Gerald Jay Sussman, Matthew Halfant

◆ January 1988 **Proceedings of the 1988 ACM conference on LISP and functional programming LFP '88**

Publisher: ACM Press

Full text available: [pdf\(673.99\)](#) Additional Information: [full citation](#), [KB](#) [citations](#), [index](#)

We illustrate how the liberal use of high-order procedural abstractions a to express some of the vocabulary and methods of numerical analysis. 1 toolbox encapsulating the technique of Richardson extrapolation, and w problems of numerical integration and differentiation. By separating the extrapolation from its use in particular circumstances we indicate how n written that exh ...

15 BIST and production testing of ADCs using imprecise stimulus

 Kumar Parthasarathy, Turker Kuyel, Dana Price, Le Jin, Degang Chen, Rar October 2003 **ACM Transactions on Design Automation of Electro**
Volume 8 Issue 4

Publisher: ACM Press

Full text available: [pdf\(500.33\)](#) Additional Information: [full citation](#), [KB](#) [index terms](#)

A new approach for testing mixed-signal circuits based upon using impr Unlike most existing Built-In Self-Test (BIST) and production test appro excitation signals that are at least 3 bits or more linear than the Device proposed approach can work with stimuli that are several bits less linea dramatically reduces the requirements on stimulus generation for BIST potential for using inexpe ...

Keywords: ADC linearity, Analog and mixed-signal testing, built-in self measurement, imprecision stimulus, production test

16 A low power high performance switched-current multiplier

D. M. W. Leenaerts, G. H. M. Joordens, J. A. Hegh

August 1996 **Proceedings of the 1996 international symposium o and design ISLPED '96**

Publisher: IEEE Press

Full text available: [pdf\(77.12\)](#) Additional Information: [full citation](#), [KB](#)

17 Low power signal processing architectures for network microsensor

 Michael J. Dong, K. Geoffrey Yung, William J. Kaiser

August 1997 **Proceedings of the 1997 international symposium o**

and design ISLPED '97

Publisher: ACM Press

Full text available: [pdf\(613.09 KB\)](#)

Additional Information: [full citation](#),

[KB](#))

18 [Session 4B: Rangesum histograms](#)

S. Muthukrishnan, Martin Strauss

January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on algorithms SODA '03**

Publisher: Society for Industrial and Applied Mathematics

Full text available: [pdf\(1.10 MB\)](#) Additional Information: [full citation](#), [citations](#), [index](#)

A *rangesum query* to an array \mathbf{A} is a pair (l, r) of range endpoints, which satisfy $l \leq i \leq r \leq n$. To compress \mathbf{A} , we consider representing an array \mathbf{A} lossily by an array \mathbf{H} such that $\mathbf{H}[i]$ is constant on each of a small number of buckets. We then answer a *rangesum query* to \mathbf{A} instead of from \mathbf{A} , i.e., as $\sum_{i=l}^r \mathbf{H}[i]$. An optimal ...

19 [Continuous speech recognition II: Spectral estimation for noise robustness](#)

Adoram Erell, Mitch Weintraub

October 1989 **Proceedings of the workshop on Speech and Natural Language Processing**

Publisher: Association for Computational Linguistics

Full text available: [pdf\(377.73 KB\)](#) Additional Information: [full citation](#), [citations](#)

We present results on the recognition accuracy of a continuous speech, recognition system that incorporates a novel noise reduction algorithm. The system uses a minimum mean square error estimation tailored for a filter-bank front-end. It achieves a significant improvement over similar published algorithms by incorporating a more accurate model for the filter-bank log-energies, and by attempting to jointly estimate the parameters of the entire vector rather than individual components ...

20 [Efficient and accurate testing of analog-to-digital converters using on-chip reference oscillators](#)

K. Arabi, B. Kaminska

March 1997 **Proceedings of the 1997 European conference on Design automation**

Publisher: IEEE Computer Society

Full text available: [pdf\(511.01 KB\)](#)

Additional Information: [full citation](#), [citations](#)

[KB](#))

Publisher
Site

This paper describes a practical test approach for analog-to-digital conv oscillation-test strategy. The oscillation-test is applied to convert the AC oscillator. The oscillation frequencies are able to monitor the ADC conve nonlinearity (DNL) and integral nonlinearity (INL) at each quantization t method, no analog stimulus should be supplied and therefore the need generato ...

Keywords: A/D convertor, ADC conversion rate, ADC testing, analog-to-analogue-digital conversion, differential nonlinearity, digital circuitry, in-oscillation-test method, quantization band edge

Results 1 - 20 of 35

Result page: [1](#) [2](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copy
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact I](#)

Useful downloads: [Adobe Acrobat](#) [QuickTime](#) [Windows Media Player](#)

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1588	(341/143).CCLS.	USPAT	OR	OFF	2007/03/29 08:36
L2	374	(341/143).CCLS.	US-PGPU B	OR	OFF	2007/03/29 08:36
L3	0	sigma NEAR1 delta second NEAR2 error feedback	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	WITH	ON	2007/03/29 08:36
L4	15	sigma NEAR1 delta second NEAR2 error feedback	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	WITH	ON	2007/03/29 08:37
L5	2	sigma NEAR1 delta second NEAR2 error feedback and I1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	WITH	ON	2007/03/29 08:37

EAST Search History

L6	2	sigma NEAR1 delta second NEAR2 error feedback and I2	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON	2007/03/29 08:41
L7	4	sigma NEAR1 delta second NEAR2 error and I2	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON	2007/03/29 08:44
L8	12	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	AND	ON	2007/03/29 08:44
L9	8	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/03/29 08:44

EAST Search History

L10	4	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON	2007/03/29 08:44
L11	2	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error and l1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	WITH	ON	2007/03/29 08:47
L12	1	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error filter	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/03/29 09:02
L13	1	sigma NEAR1 delta subtract\$5 NEAR3 second NEAR2 error filter and l1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	SAME	ON	2007/03/29 09:02